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RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2133

PATENT APPLICATION
Docket No.: 4591-220
Client Ref. No.: ID10069-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yong JEONG and Sung-Soo LEE

Serial No.: 10/003,390 Examiner: Tu, Christine Trinh Le

Filed: October 30, 2001 Group Art Unit: 2133

Confirmation No.: 5348

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE WITH A
FAIL BIT DETECTING SCHEME AND METHOD FOR
COUNTING THE NUMBER OF FAIL BITS

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

*ok to enter
per RCE
6-24-05*

AMENDMENT AFTER FINAL REJECTION UNDER 37 CFR 1.116

Responsive to the Final Office Action, Paper No. 20050222, dated February 25, 2005,
please amend the application as follows.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Drawings begin on page 3 of this paper and include both an
attached replacement sheet and an annotated sheet showing changes.

Amendments to the Claims are reflected in the listing of claims which begins on
page 4 of this paper.

Remarks/Arguments begin on page 11 of this paper.

An Appendix including amended drawing figures is attached following page 14 of
this paper.

Docket No. 4591-220

Page 1 of 14

Application No. 10/003,390